



Good things come in small packages, We help good become better

# **SEMICONDUCTORS**

HCL ENGINEERING AND R&D SERVICES



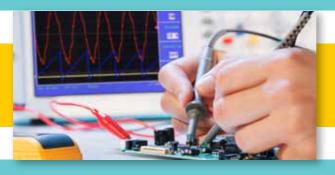
# The Environment Today

The cyclical nature of the semiconductor industry is proving to be quite challenging and semiconductor companies are struggling to boost the utilization of capital-intensive equipment in a time of fluctuating demand. They also face the constant threat of obsolescence from competitors with disruptive technologies. Innovative technologies can help them find the right balance between growth and profitability.

# Semiconductors Capabilities

- Feasibility Studies
- Styling & Ergonomics

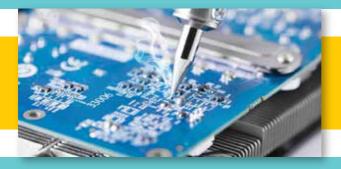
- Concept Generation
- Packaging Study



Engineering Analysis

- CAD / CAE software customization
- Process Automation

• PDM / PLM Software Services



Manufacturing Engineering Services

• In-house test facilities



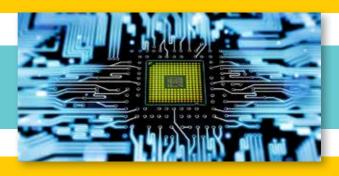
Technical Publication

### Product Design



- Cabling, Piping & Routing schematics
- Engineering Drawing Creation / Conversion

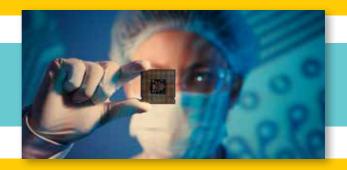
### Engineering Automation



• In-house Prototyping machines

• Sheet metal, Plastic Jigs and Fixture design

# Testing Services



• Illustrated Parts catalogue

• Wiring and piping diagrams

## Services and Solutions

Semi Equipment



### **Mechanical Engineering**

- Wafer Tester Design
- Radiation Enclosure, PDU
- Facilities, End effector
- Racks & Chassis, Gas Box
- Harness, Cabling, Piping
- CAE: FEA, FEM, CFD
- Simulation

### Hardware Engineering

- Board Design
- Device Drivers
- Control Systems
- Protocols EtherCAT, MODBUS, DeviceNet
- Embedded Design
- Diagnostics & BSP

### **Software Engineering**

- Control Systems Software
- Yield Improvement
- Image processing
- Equipment Application
- Re architecture
- Recipe Creation & Management
- Legacy RTOS Porting
- SEMI UI Standards Implementation

### Life Cycle Support

- Technical Publications
- Prototyping
- Low Volume Manufacturing
- Compliance Testing
- Customer Field Support
- PDM / PLM Services
- IT Services

## Services and Solutions

### Semi Chip

#### **Implementation**

High level architecture Micro Architecture RTL Implementing Module level testing Integration

#### **System Modeling**

Component Modeling Interconnect Modeling Reusable Libraries Platform Modeling Conversion Tools

#### Post Silicon Validation

Board design, assembly, testing FW and software design Board bring up; OS and application loading System level verification I/O and timing parameters

#### Verification

IP verification (HCL and 3rd party)
Functional verification
Protocol monitors and checkers
Assertion based verification
Bus functional model
Code coverage analysis

#### **ASIC Development**

Technology mapping,
Synthesis Physical Design
Timing closure
Speed, power analysis
DFT insertion
Formal verification

#### **FPGA**

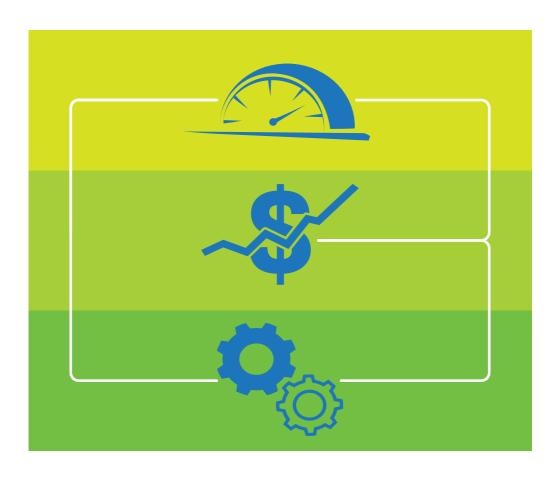
#### **Prototyping**

timing closure
Configuration file
generation
FPGA concept proof
ASIC to FPGA and FPGA
to ASIC conversion

Synthesis, place route,



# Service Line Units



#### Acceleration

• Platform Acceleration Suite

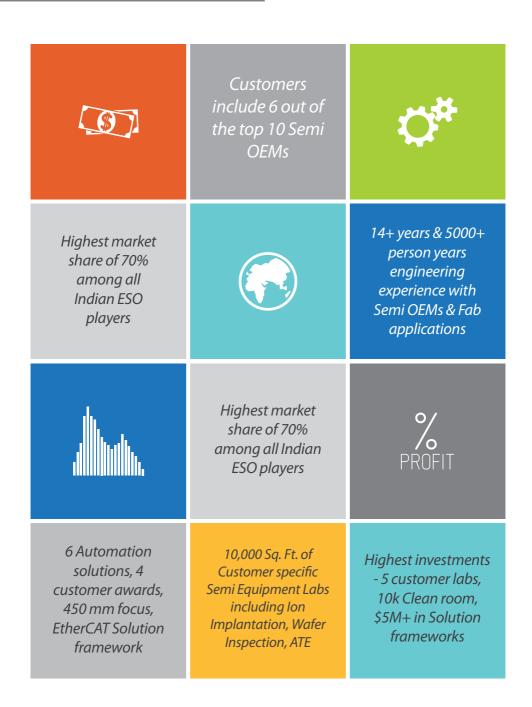
#### **Value**

- eDAT
- VAVE

#### **Technology**

- Product Intelligence
- Internet of Things

# HCL in Semiconductors

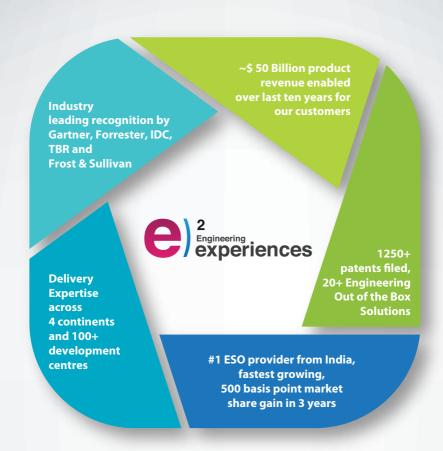


# <u>Experiences Engineered</u> for our customers (Case Studies)

- Reduction of cost by 20% accumulating to \$60M in 10 years and acceleration migration to new platforms & reduction of time to market for leading semi ATE
- Setup customer specific labs and clean rooms for autonomy of projects and established a collaborative engagement model with customer's India captive center. Cost savings of \$150M in 12 years
- Balanced new product engineering vs. sustenance and non-core activities. Cyclical market impacting time to market. Final result was \$30M cumulative cost savings
- Supporting Customers of a leading chip manufacturer for Porting, Bug-fixing and Enhancements of Intel Multi Application Framework for Set-Top Box and Home gateways. Redefine Build process to increase efficiency and automate wherever possible
- Porting of Fedora packages on Customer Platform. Porting of Debian packages on Customer's Platform. End Customers able to put 3rd party applications on Fedora/Debian









Hello, I'm from HCL's Engineering and R&D Services. We enable technology led organizations to go to market with innovative products and solutions. We partner with our customers in building world class products and creating associated solution delivery ecosystems to help bring market leadership. We develop engineering products, solutions and platforms across Aerospace and Defense, Automotive, Consumer Electronics, Software, Online, Industrial Manufacturing, Medical Devices, Networking and Telecom, Office Automation, Semiconductor and Servers & Storage for our customers.

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